

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR  
COLLEGE OF ENGINEERING (AUTONOMOUS) :: PULIVENDULA  
PULIVENDULA - 516 390, Y.S.R. (Dist), Andhra Pradesh, INDIA**

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Date: 22.09.2021

**REVISED NOTICE**

All the Students of M.Tech I Year II Semester (R17) are informed that as per the College academic calendar 1st Mid Examinations of 2020 Admitted Batch for all Branches will commence from **28.09.2021** onwards.

**(Timings: 09:30A.M-12:30P.M 2.30 P.M-4.30 P.M.)**

Date	EPS	PEE	CAD/CAM	DECS	CSE
<b>28.09.2021 (FN) (Tuesday)</b>	RESTRICTURED POWER SYSTEMS (17D07203)	DIGITAL SIGNAL PROCESSING APPLICATION (17D83201)	ADVANCED IN OPTIMIZATION TECHNIQUES (17D04201)	WIRELESS COMMUNICATIONS (17D38201)	NETWORK PROGRAMMING (17D58201)
<b>28.09.2021 (AN) (Tuesday)</b>	ADVANCED POWER SYSTEMS PROTECTION (17D07202)	ADVANCED ELECTRIC DRIVERS (17D83202)	ROBOTICS (17D04202)	DETECTION AND ESTIMATION THEORY (17D38202)	DATA ANALYTICS (17D58203)
<b>29.09.2021 (FN) (Wednesday)</b>	POWER SYSTEMS STABILITY & CONTROL (17D07201)	MODERN POWER ELECTRONICS (17D83203)	CNC PROGRAMMING & TECHNOLOGY (17D04203)	MIXED SIGNAL DESIGN (17D38203)	ADVANCED INTERNET OF THINGS (17D58202)
<b>29.09.2021 (AN) (Wednesday)</b>	FACTS CONTROLLERS (17D07209)(ELECTIVE IV)	ELECTRIC TRACTION SYSTEMS (17D83204) (ELECTIVE III)	HYDRAULIC AND PNEUMATIC CIRCUITS (17D04206) (ELECTIVE III)	EMBEDDED SYSTEM DESIGN (17D38204) (ELECTIVE III)	SOFTWARE ARCHITECTURE AND DESIGN PATTERNS (17D58204) (ELECTIVE III)
<b>30.09.2021 (FN) (Thursday)</b>	ENERGY AUDITING CONSERVATION & MANAGEMENT (17D07205) ELECT-III	DIGITAL CONTROL SYSTEMS (17D83209) (ELECTIVE IV)	COMPOSITE MATERIALS (17D04208) Elect-IV	MULTIMEDIA COMMUNICATIONS (17D38209) Elect-IV	CYBER SECURITY AND DIGITAL FORENSICS (17D58208) (ELECT- IV)

All the above students are instructed to note the same and follow the Examination Schedule.

Copy to:

1. All Notice boards
2. File.

  
PRINCIPAL